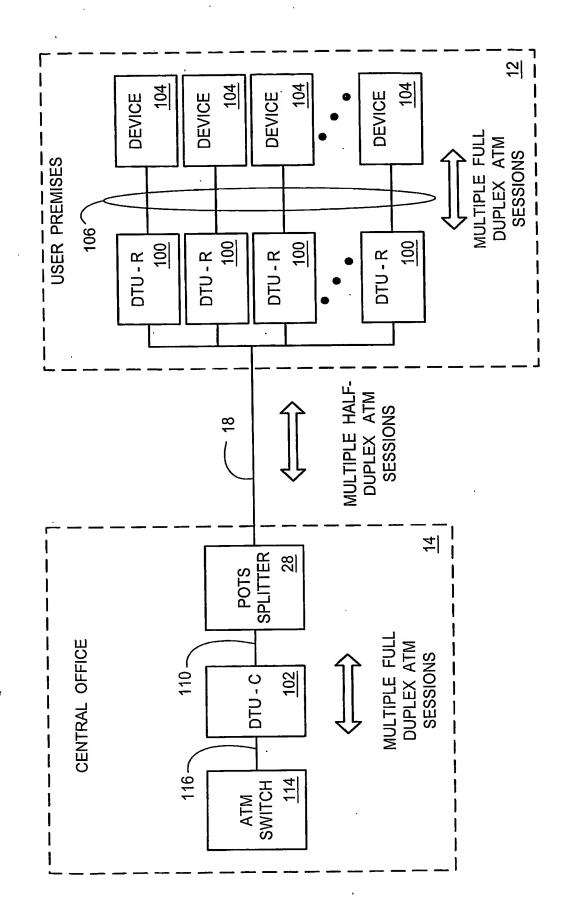
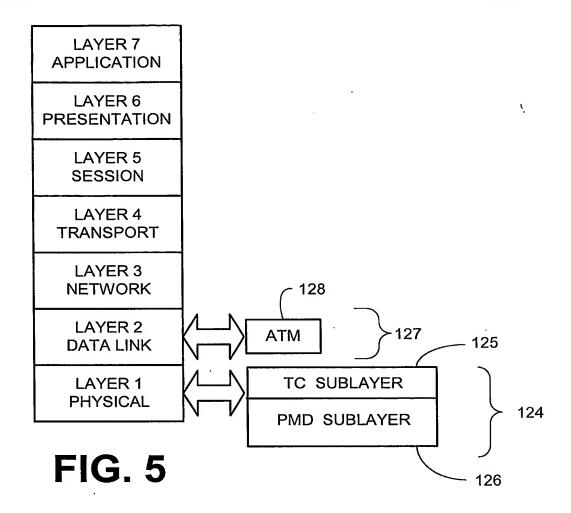
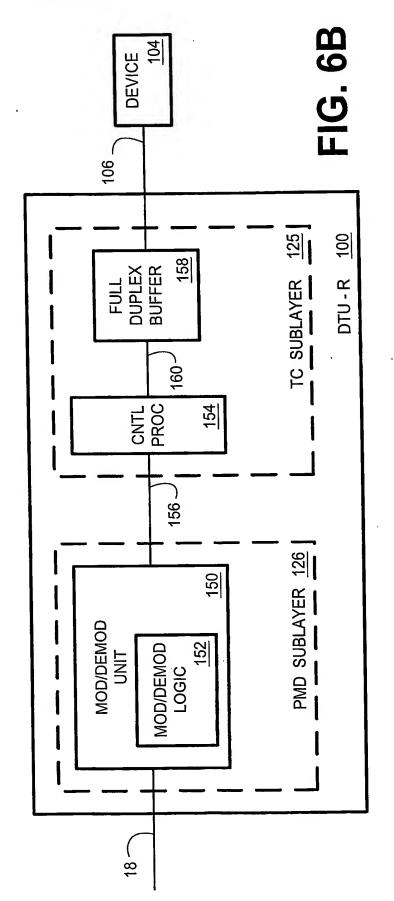


FIG. 4





110~ 102 PMD SUBLAYER 126 144 DTU-C MOD/DEMOD UNIT 146 MOD/ DEMOD LOGIC 142 TC SUBLAYER 125 CNTL PROC/ DIGITAL MUX 138 140c — 140d 140a 140b 136 130 FULL DUPLEX 132 FULL DUPLEX 134 FULL DUPLEX FULL DUPLEX BUFFERS BUFFERS BUFFERS BUFFERS FIG. 6A 38d (38b — 380 38a /



176n 53 OCTET ATM CELL 176b 53 OCTET ATM CELL 176a 53 OCTET ATM CELL ADMIN HDR ' 174 172 PREAMBLE

FIG. 7

FIG. 8A

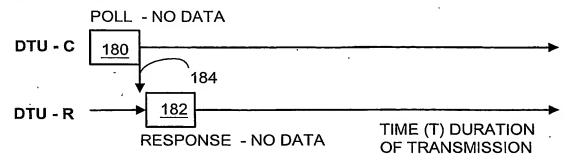


FIG. 8B

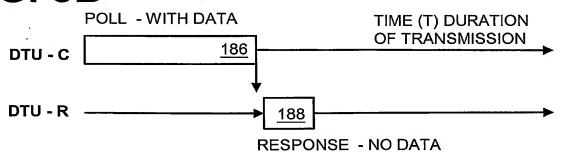


FIG. 8C

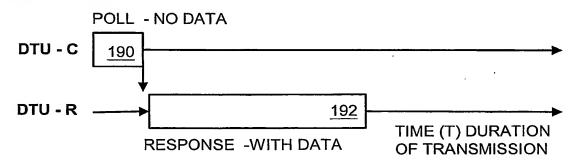
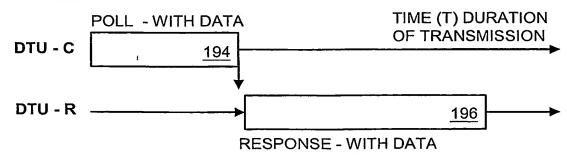


FIG. 8D



15

DOWNSTREAM 200	UP- STREAM 204	DOWNSTREAM 202	UP- STREAM <u>206</u>
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FIG. 9A

T UPSTREAM L	DOWN- STREAM 212	UPSTREAM · 210	DOWN- STREAM 214	
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FIG. 9B

[
DOWNSTREAM I	UPSTREAM	DOWNSTREAM	UPSTREAM
<u>216</u>	<u>220</u>	<u>218</u>	. <u>222</u>
<u></u>			

FIG. 9C

UPSTREAM <u>224</u>	DOWN- STREAM 230	UPSTREAM <u>226</u>	DOWNSTREAM	UP- STREAM- 232
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FIG. 9D